


First Named Inventor	Frankie F. Roohparvar	INFORMATION DISCLOSURE STATEMENT FORM PTO-1449
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Sheet 1 of 2

U.S. Patent References				
Examiner Initials	Document/Patent No.	Issue/Publication Date	Name	Filing Date
BYP	5,041,886	08/20/1991	Lee	09/25/1989
BYP	5,657,292	08/12/1997	McClure	01/19/1996
BYP	5,600,605	02/04/1997	Schaefer	06/07/1995
BYP	5,602,781	02/11/1997	Isobe	03/16/1995
BYP	5,657,292	08/12/1997	McClure	01/19/1996
BYP	5,666,321	09/09/1997	Schaefer	09/01/1995
BYP	5,666,322	09/09/1997	Conkle	09/21/1995
BYP	5,732,017	03/24/1998	Schumann et al.	03/31/1997
BYP	5,751,039	05/12/1998	Kauffman et al.	09/19/1997
BYP	5,787,457	07/28/1998	Miller et al.	10/18/1996
BYP	5,808,946	09/15/1998	Roohparvar	03/27/1997
BYP	5,851,876	12/22/1998	Jeng	05/14/1998
BYP	5,867,430	02/02/1999	Chen	12/20/1996
BYP	5,885,866	03/23/1999	Chen	06/11/1997
BYP	5,889,727	03/30/1999	Hsu et al.	05/11/1998
BYP	5,913,928	06/22/1999	Morzano	06/22/1999
BYP	5,936,903	08/10/1999	Jeng et al.	08/07/1998
BYP	5,956,587	09/21/1999	Chen et al.	02/17/1998
BYP	5,959,887	09/28/1999	Takashina et al	02/24/1998
BYP	5,995,438	11/30/1999	Jeng, et al.	08/07/1998
BYP	6,011,751	01/04/2000	Hirabayashi	12/21/1998
BYP	6,026,465	02/15/2000	Mills et al.	06/18/1997
BYP	6,077,211	06/20/2000	Vo	02/27/1998
BYP	6,137,133	10/24/2000	Kauffman et al.	05/11/1998
BYP	6,141,247	10/31/2000	Roohparvar et al.	12/21/1998
BYP	6,240,040	05/29/2001	Akaogi et al.	03/15/2000
BYP	6,418,506	07/09/2002	Pashley et al.	12/31/1996

Foreign Patent References				
Examiner Initials	Foreign Patent Country	No.	Name	Publication Date

Other References	
Examiner Initials	Author, Title, Date, Pages, etc.
BYP	Keeth, et al., "DRAM circuit design: a tutorial," IEEE Press, 2001, pp.16-23, 142-153
BYP	Micron Semiconductor Products, Inc., "2Mb, Smart 5 BIOS-Optimized Boot Block Flash Memory," <u>Flash Memory</u> www.micron.com, copyright 2000, Micron Technology, Inc., pp. 1-12
BYP	Micron, "16 Mb: x16 SDRAM" <u>Synchronous DRAM</u> , www.micron.com, copyright 1999 Micron Technology, Inc., pp. 1-51

Examiner Signature		Date Considered	9/27/04
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*Examiner: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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